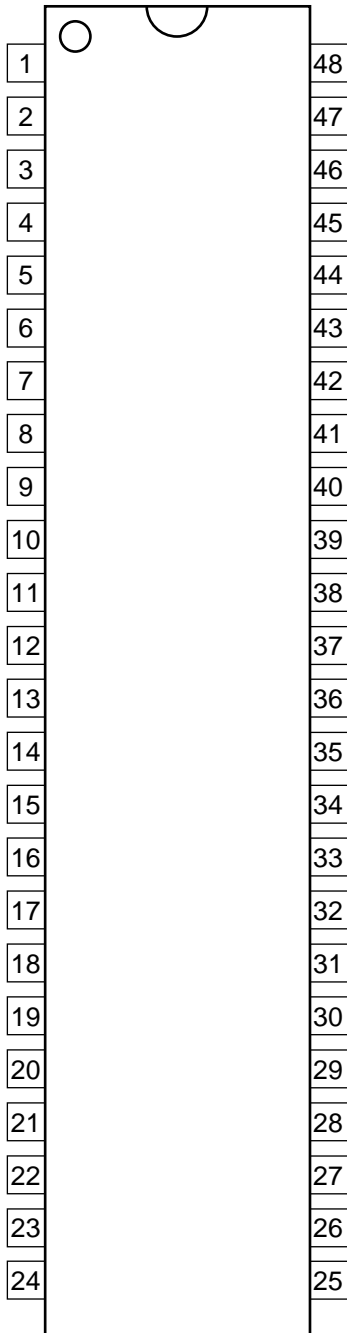


16-BIT INVERTING BUFFER/LINE DRIVER

—TOP VIEW—

**INPUTS**

I0 - I15 : INPUT

 $\overline{OE1} - \overline{OE4}$: OUTPUT ENABLE**OUTPUTS** $\overline{O0} - \overline{O15}$: OUTPUT

| INPUTS | | OUTPUTS |
|------------------|---------|---------------------------------|
| $\overline{OE1}$ | I0 - I3 | $\overline{O0} - \overline{O3}$ |
| 0 | 0 | 1 |
| 0 | 1 | 0 |
| 1 | x | HI-Z |

| INPUTS | | OUTPUTS |
|------------------|---------|---------------------------------|
| $\overline{OE2}$ | I4 - I7 | $\overline{O4} - \overline{O7}$ |
| 0 | 0 | 1 |
| 0 | 1 | 0 |
| 1 | x | HI-Z |

| INPUTS | | OUTPUTS |
|------------------|----------|----------------------------------|
| $\overline{OE3}$ | I8 - I11 | $\overline{O8} - \overline{O11}$ |
| 0 | 0 | 1 |
| 0 | 1 | 0 |
| 1 | x | HI-Z |

| INPUTS | | OUTPUTS |
|------------------|-----------|-----------------------------------|
| $\overline{OE4}$ | I12 - I15 | $\overline{O12} - \overline{O15}$ |
| 0 | 0 | 1 |
| 0 | 1 | 0 |
| 1 | x | HI-Z |

0 : LOW LEVEL

1 : HIGH LEVEL

x : DON'T CARE

HI-Z : HIGH IMPEDANCE

| PIN NO. | I/O | SIGNAL | PIN NO. | I/O | SIGNAL | PIN NO. | I/O | SIGNAL | PIN NO. | I/O | SIGNAL |
|---------|-----|-------------------------|---------|-----|-------------------------|---------|-----|-------------------------|---------|-----|-------------------------|
| 1 | I | $\overline{\text{OE}}1$ | 13 | O | $\overline{\text{O}}8$ | 25 | I | $\overline{\text{OE}}3$ | 37 | I | I7 |
| 2 | O | $\overline{\text{O}}0$ | 14 | O | $\overline{\text{O}}9$ | 26 | I | I15 | 38 | I | I6 |
| 3 | O | $\overline{\text{O}}1$ | 15 | — | GND | 27 | I | I14 | 39 | — | GND |
| 4 | — | GND | 16 | O | $\overline{\text{O}}10$ | 28 | — | GND | 40 | I | I5 |
| 5 | O | $\overline{\text{O}}2$ | 17 | O | $\overline{\text{O}}11$ | 29 | I | I13 | 41 | I | I4 |
| 6 | O | $\overline{\text{O}}3$ | 18 | — | Vcc | 30 | I | I12 | 42 | — | Vcc |
| 7 | — | Vcc | 19 | O | $\overline{\text{O}}12$ | 31 | — | Vcc | 43 | I | I3 |
| 8 | O | $\overline{\text{O}}4$ | 20 | O | $\overline{\text{O}}13$ | 32 | I | I11 | 44 | I | I2 |
| 9 | O | $\overline{\text{O}}5$ | 21 | — | GND | 33 | I | I10 | 45 | — | GND |
| 10 | — | GND | 22 | O | $\overline{\text{O}}14$ | 34 | — | GND | 46 | I | I1 |
| 11 | O | $\overline{\text{O}}6$ | 23 | O | $\overline{\text{O}}15$ | 35 | I | I9 | 47 | I | I0 |
| 12 | O | $\overline{\text{O}}7$ | 24 | I | $\overline{\text{OE}}4$ | 36 | I | I8 | 48 | I | $\overline{\text{OE}}2$ |

